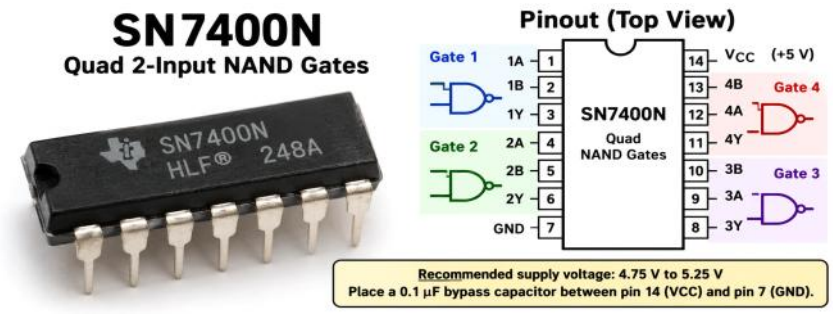
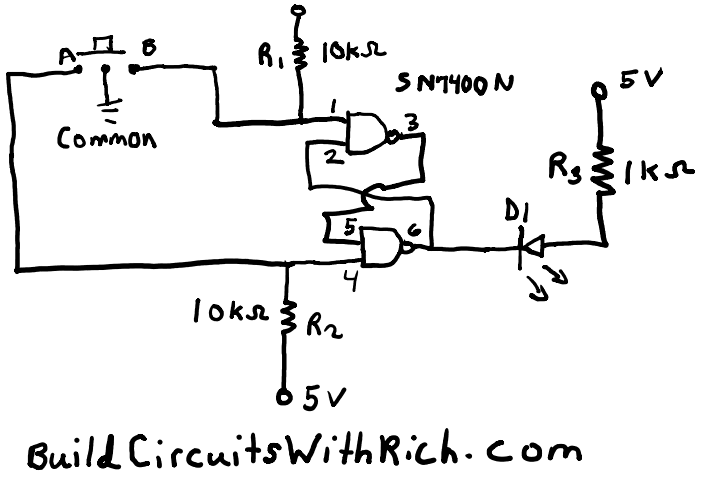
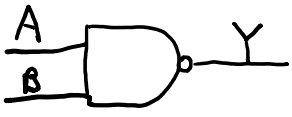
How to Eliminate Switch Bounce Using NAND Gates!







SN7400N — NAND Truth Table

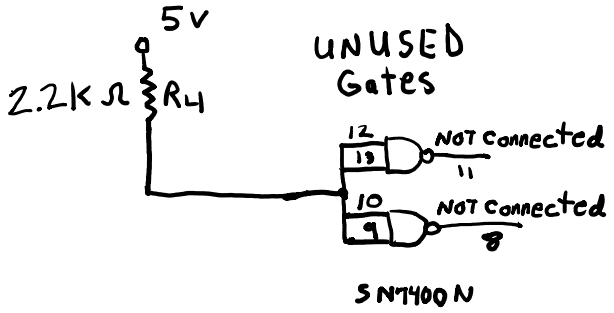
Input A	Input B	Output Y
0	0	1
0	1	1
1	0	1
1	1	0

0 = LOW | 1 = HIGH

Remember: A NAND gate produces a **LOW** output only when both inputs are HIGH.

The first gate uses pin 1 (A), pin 2 (B), and pin 3 (Y). The second gate uses pin 4 (A), pin 5 (B), and pin 6 (Y).

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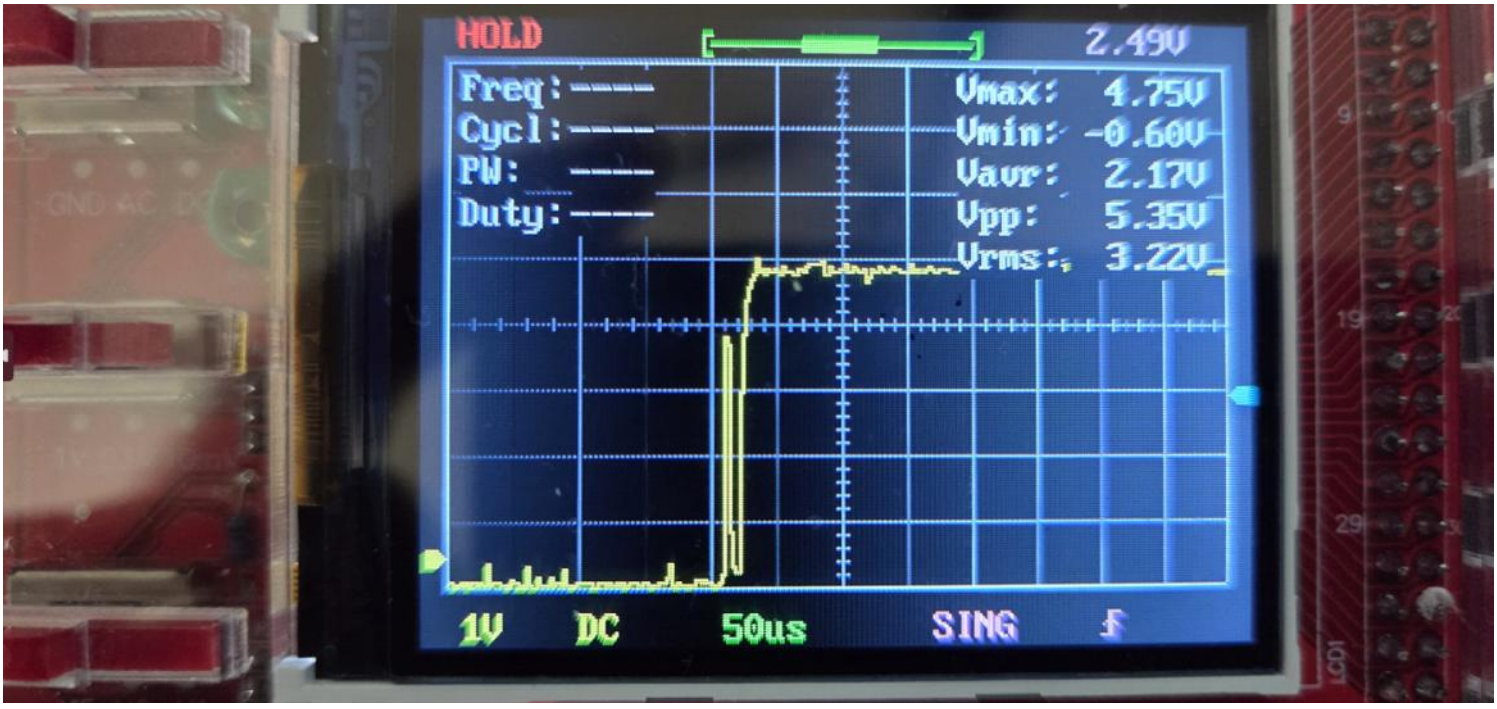
Pin 7 - Gnd

Pin 14 - 5V

0.1μF capacitor between pin 14 (5V) and pin 7 (GND)

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Raw Mechanical Switch Bounce



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