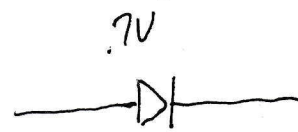
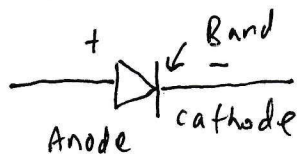
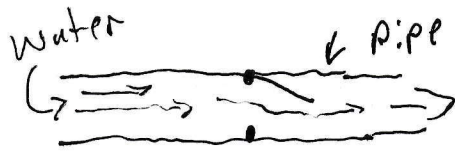
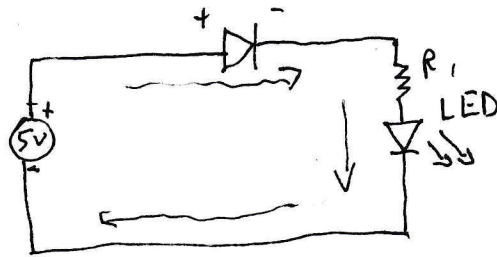


# Diodes and Diode Logic

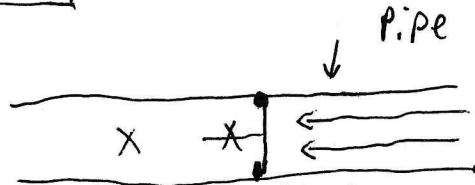
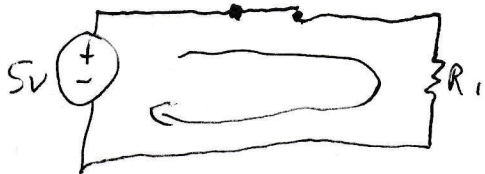
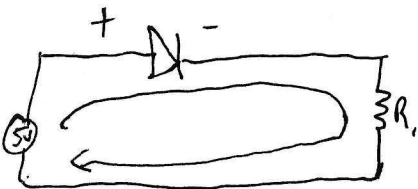
①



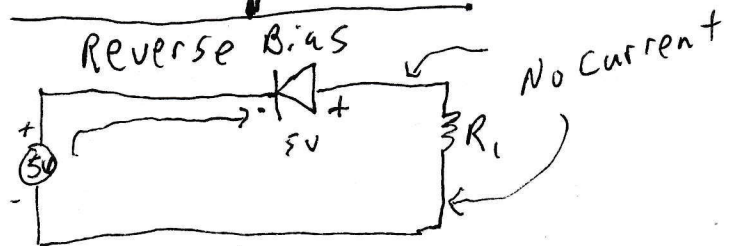
.7V forward bias Voltage



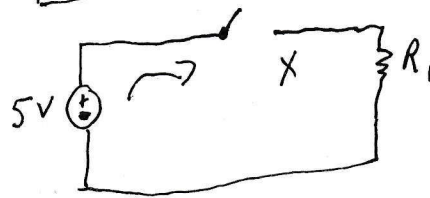
Forward Bias



Reverse Bias



No Current



Diode = ON = 1 } Binary  
 Diode = OFF = 0 }

Diode = 5V

Diode = 0V

# Diodes and Diode Logic

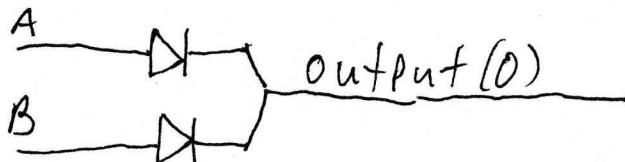
(2)

## Diodes as Logic Gates:

### OR Gate:

#### Truth Table

Input		Output
A	B	O
0V	0V	0V = Low
0V	5V	5V = H:
5V	0V	5V = H:
5V	5V	5V = H:



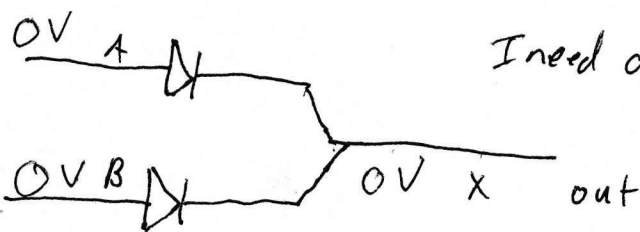
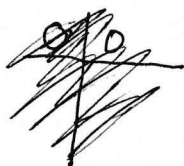
5V = ON  
0V = OFF

OR Gate

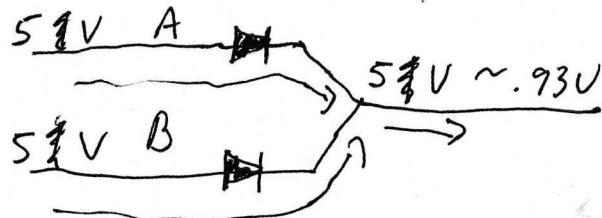
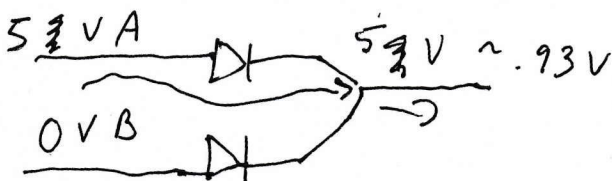
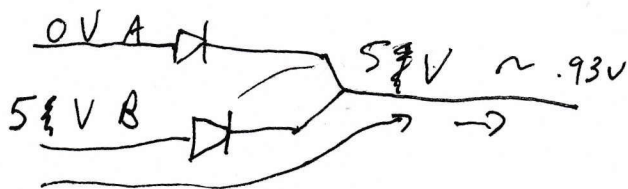
Binary

2<sup>3</sup> 2<sup>2</sup> 2<sup>1</sup> 2<sup>0</sup> Binary

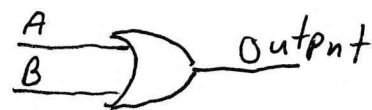
8 4 2 1 Dec



I need at least .7V for forward bias



OR Gate



OR Gates

AND Gates

NOR Gates

NAND Gates

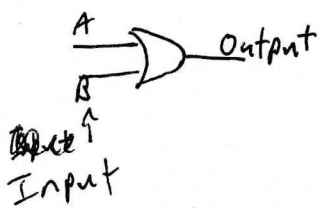
# Diodes and Diode Logic

(3)

## Truth Tables

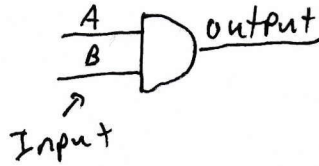
OR

A	B	output
0	0	0
0	1	1
1	0	1
1	1	1



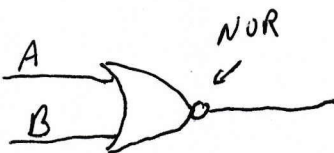
AND

A	B	output
0	0	0
0	1	0
1	0	0
1	1	1



NOR

A	B	output
0	0	1
0	1	0
1	0	0
1	1	0



NAND

A	B	output
0	0	1
0	1	1
1	0	1
1	1	0

